

[Web](#) [Images](#) [Videos](#) [Maps](#) [News](#) [Shopping](#) [Gmail](#) [more ▾](#)
[Sign in](#)

# Google scholar

  
 Scholar  Articles and patents  anytime  include citations

Results 1 - 10 of about 3,050. (0.15 sec)

[CITATION] A monolithic 156 Mb/s clock and data recovery PLL **circuit** using the sample-and-hold technique

N Ishihara, Y Akazawa - IEEE Journal of Solid-State Circuits, 1994

[Cited by 22](#) · [Related articles](#) · [S1 Circuits](#) · [All 4 versions](#)

[CITATION] Low-jitter process-independent DLL and PLL based on self-biased techniques

JG Matias - IEEE Journal of Solid-State Circuits, 1996

[Cited by 555](#) · [Related articles](#) · [S1 Circuits](#) · [All 21 versions](#)

[Download \[PDF\]](#)

RF power sensor having improved linearity over greater dynamic range

SC Crope, IR Allen - US Patent 5,214,613, 1993 - Google Patents

... Greater input signal sensitivity is achieved since the voltages developed across the **bypass capacitors** 41, 42 ... 4, a schematic circuit diagram of an Improved balanced detector is illustrated. ... This causes the input voltage standing wave ratio ("VSWR") of the sensor circuit to deteriorate ...

[Cited by 11](#) · [Related articles](#) · [S1 Circuits](#) · [All 3 versions](#)

[Download \[PDF\]](#)

[PDF] Power supply noise reduction

F Kusden - The Designer's Guide, 2004 - CiteSeer

... problem can usually be minimized by tying the two supplies together with a **bypass capacitor**, as in ... A little effort during circuit board layout can save a lot of time and minimize ... Doubtless of Tektronix for sharing some of their deep experience of real world **design** issues with ...

[Cited by 12](#) · [Related articles](#) · [View at HTML](#) · [All 12 versions](#)

[Download \[PDF\]](#)

[PDF] Three-stage large capacitive load amplifier with damping-factor-control frequency compensation

PN Leung, PIK Mol, WH Ko, JKO ... - IEEE Journal of Solid-State Circuits, 2003 - ieeexplore.ieee.org

... are short-circuited at high frequencies, and the high-frequency supply voltage **bypass** through the ... the NMIC amplifier driving a 1000-pF load requires unrealistic large compensation **capacitors** pF and ... [9] GC Temes and JW LaPstra, Introduction to **Circuit Synthesis** and ...

[Cited by 73](#) · [Related articles](#) · [View at HTML](#) · [All 13 versions](#)

[Download \[PDF\]](#)

Microelectrode amplifier with improved method of input-capacitance neutralisation

MV Thomas - Medical and Biological Engineering and Computing, 1977 - Springer

... The **bypass capacitors** C4 and C5 should have a very low impedance at high frequency; hence tetraum devices are recommended (normal electrolytic capacitors can have a significant series inductance). ... Table 1. Performance details of the circuit shown in Fig. ...

[Cited by 17](#) · [Related articles](#) · [All 2 versions](#)

[CITATION] Power line filter design considerations for dc-dc converters

DM Michalek - IEEE Industry Applications Magazine, 1999

[Cited by 21](#) · [Related articles](#) · [All 4 versions](#)

Radio-frequency impedance measurements using a tunnel-diode oscillator technique

H Sikanti, J Wiggins, H Rees - Review of Scientific Instruments, 1966 - ieeexplore.org

... The resistors and make up a voltage divider and is a **bypass capacitor** ... For C, surface mount high-Q rf chip **capacitors** (American Technical Ceramics) with values ranging from 470 to ... The LC circuit should be considered as an circuit because of the inherent internal resistance of ...

[Cited by 23](#) · [Related articles](#) · [View at PDF](#)

[Download \[PDF\]](#)

Pixel detectors with local intelligence: An IC designer point of view

F Krummenacher - Nuclear Instruments and Methods in Physics Research, 1991 - Elsevier

... 3.2. Shaper An elementary shaper amplifier is shown in Fig. 3a: the high-pass characteristic is implemented by means of **bypass capacitor** CHP and the low-pass transfer function can be realized with (parasitic) capacitor CLP. ... II. CIRCUIT DESIGN 530 VSS Fig. ...

[Cited by 46](#) · [Related articles](#) · [All 2 versions](#)

[CITATION] A 90-dB SNR 2.5-MHz Output-Rate ADC Using Cascaded Multibit Delta-Sigma Modulation at 8 Oversampling Ratio

I Fujimori, LL Tong, A Harapong, K Selyama, S Koso, ... - IEEE Journal of Solid-State ... 2000

[Cited by 130](#) · [Related articles](#) · [S1 Circuits](#) · [All 5 versions](#)

[Create email alert](#)

Google Scholar

Result Page: 1 2 3 4 5 6 7 8 9 10 [Next](#)

[Go to Google Home](#) · [About Google](#) · [About Google Scholar](#)

©2010 Google